

FIG. 1

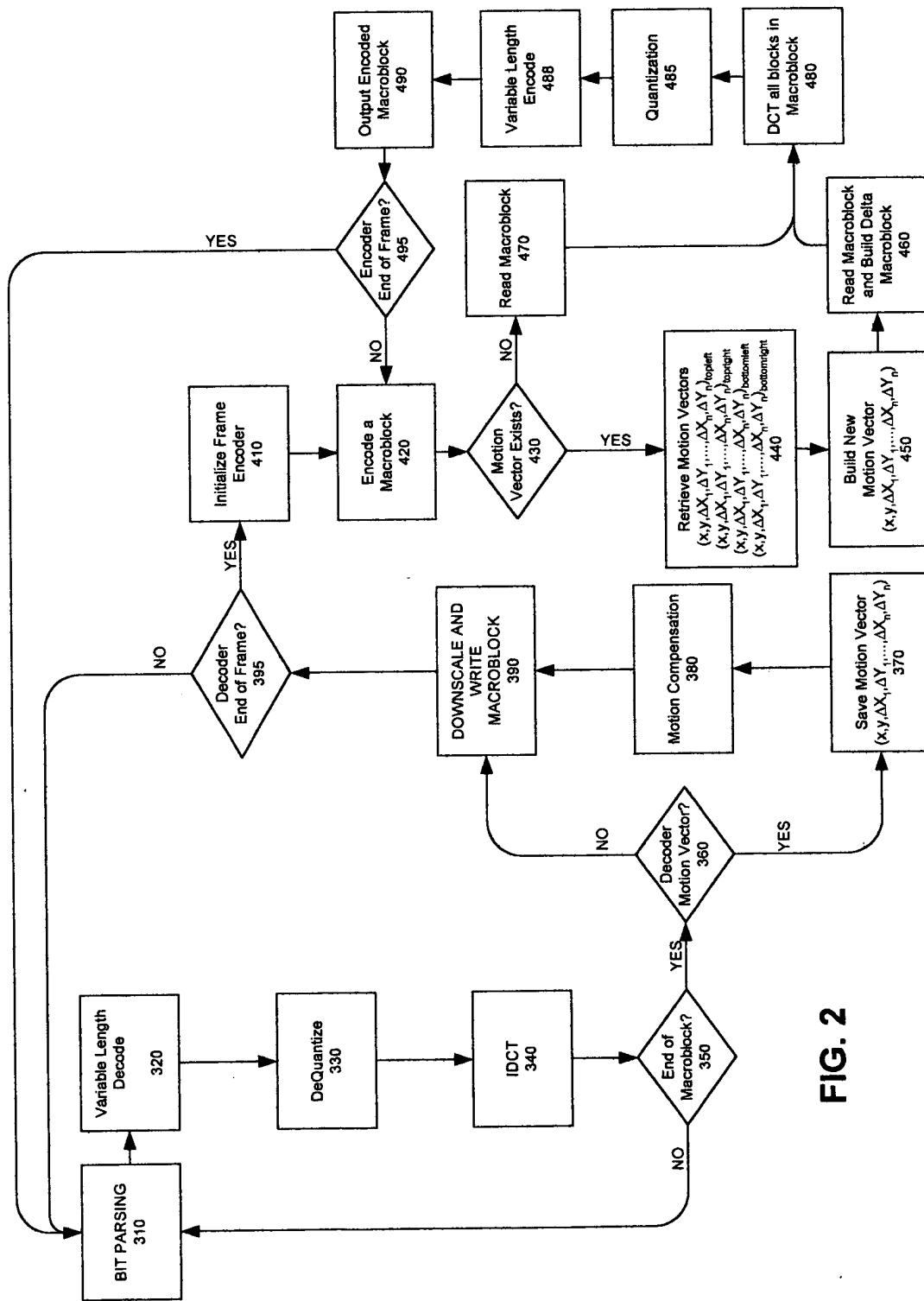


FIG. 2

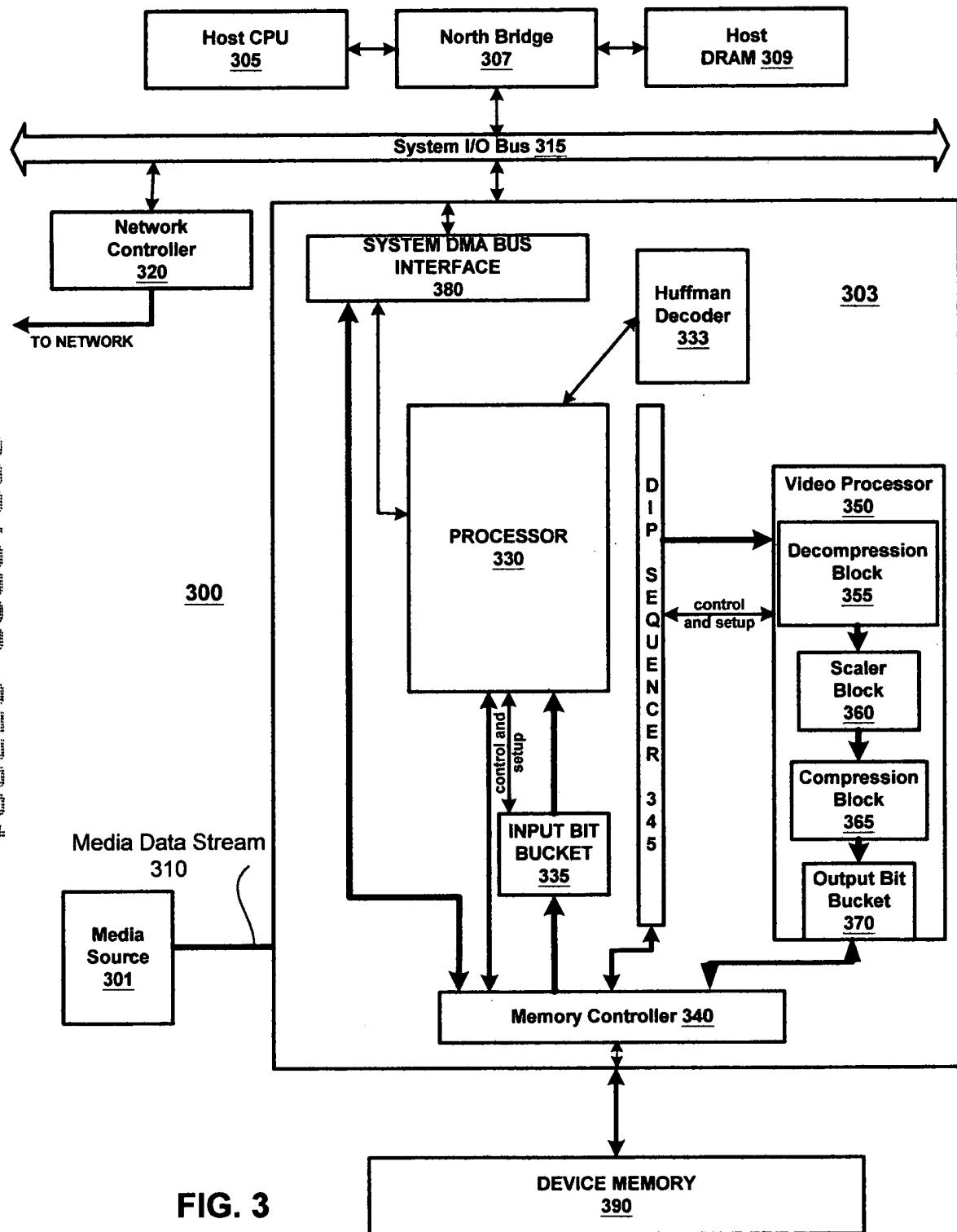


FIG. 3

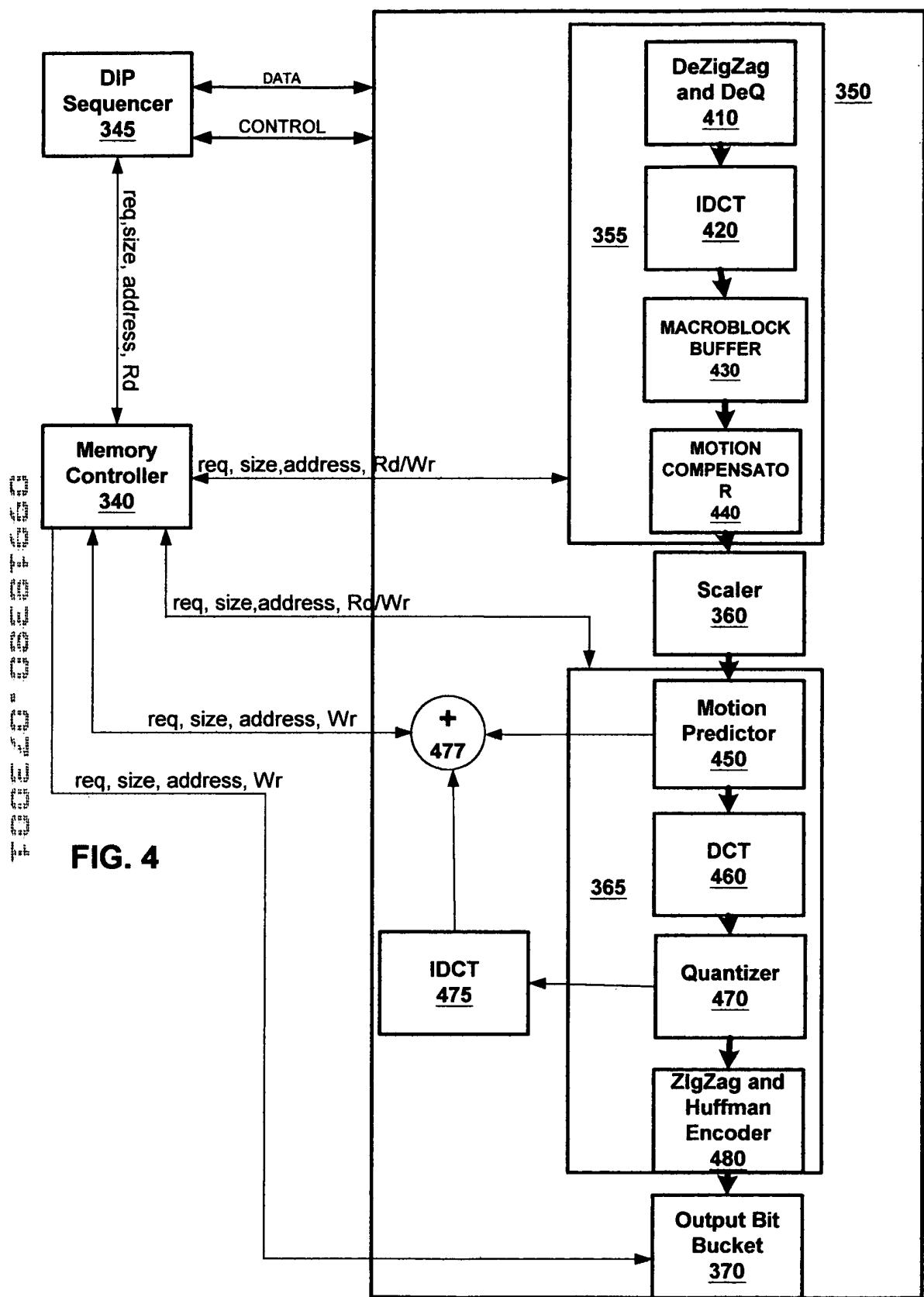


FIG. 4

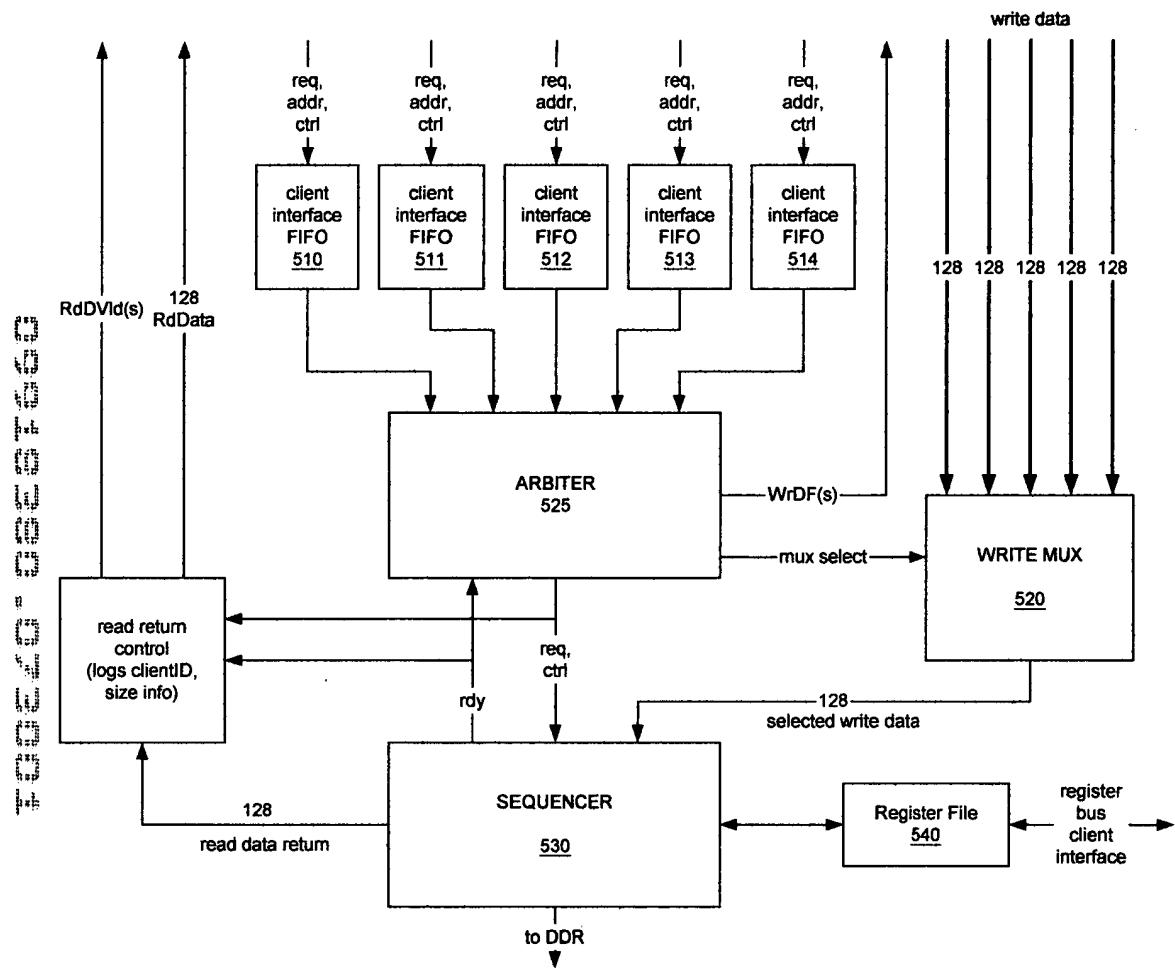
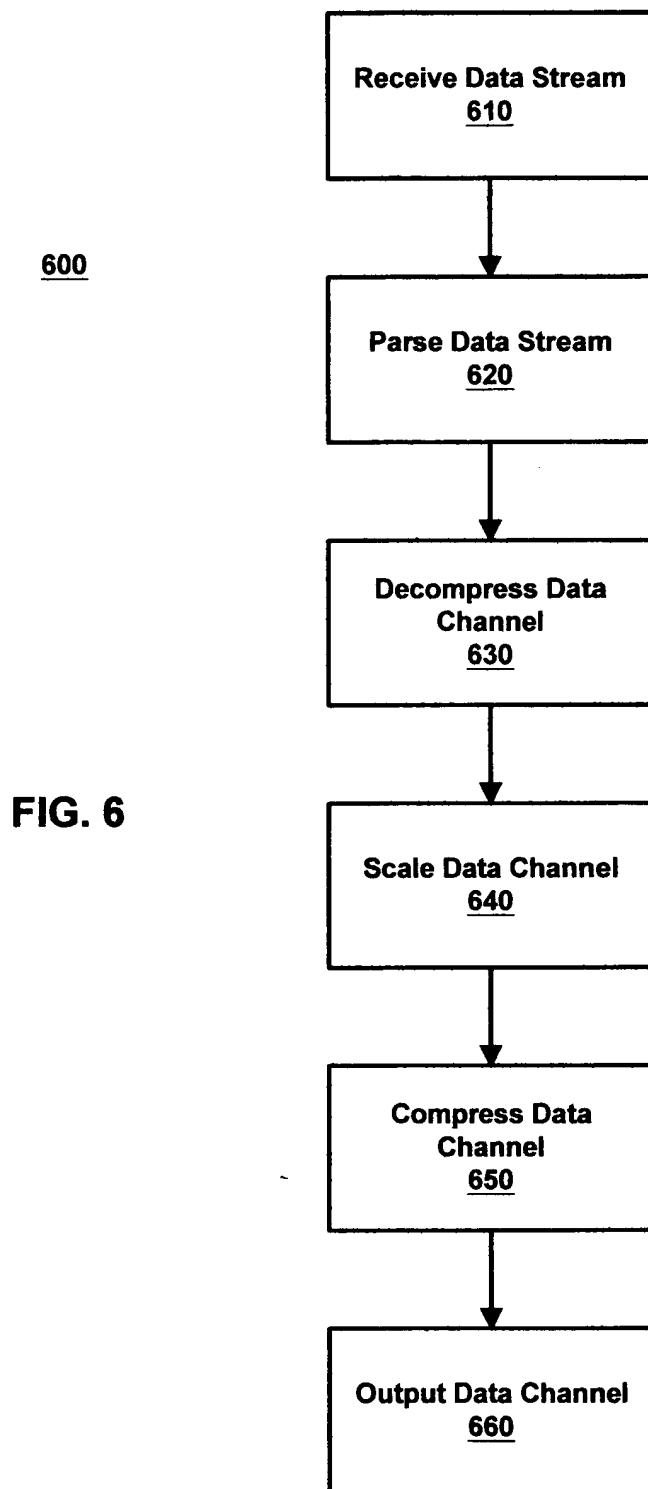


FIG. 5



0000 0000 0000 0000 0000 0000 0000 0000

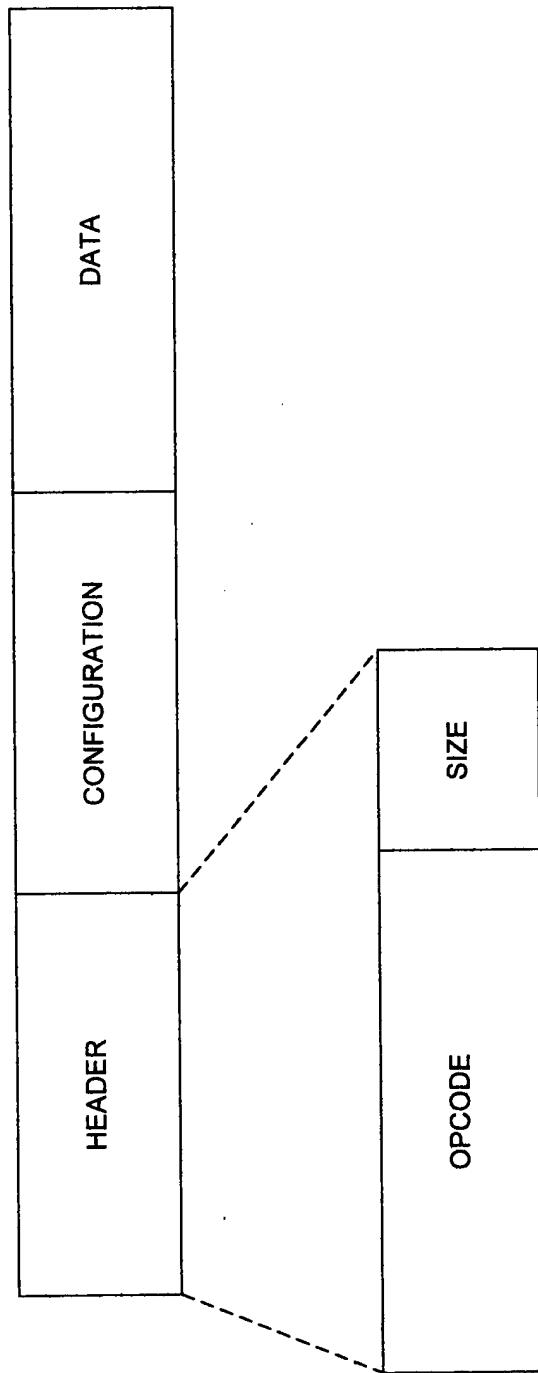


FIG. 7

710 711 712 713 390 392 393 715

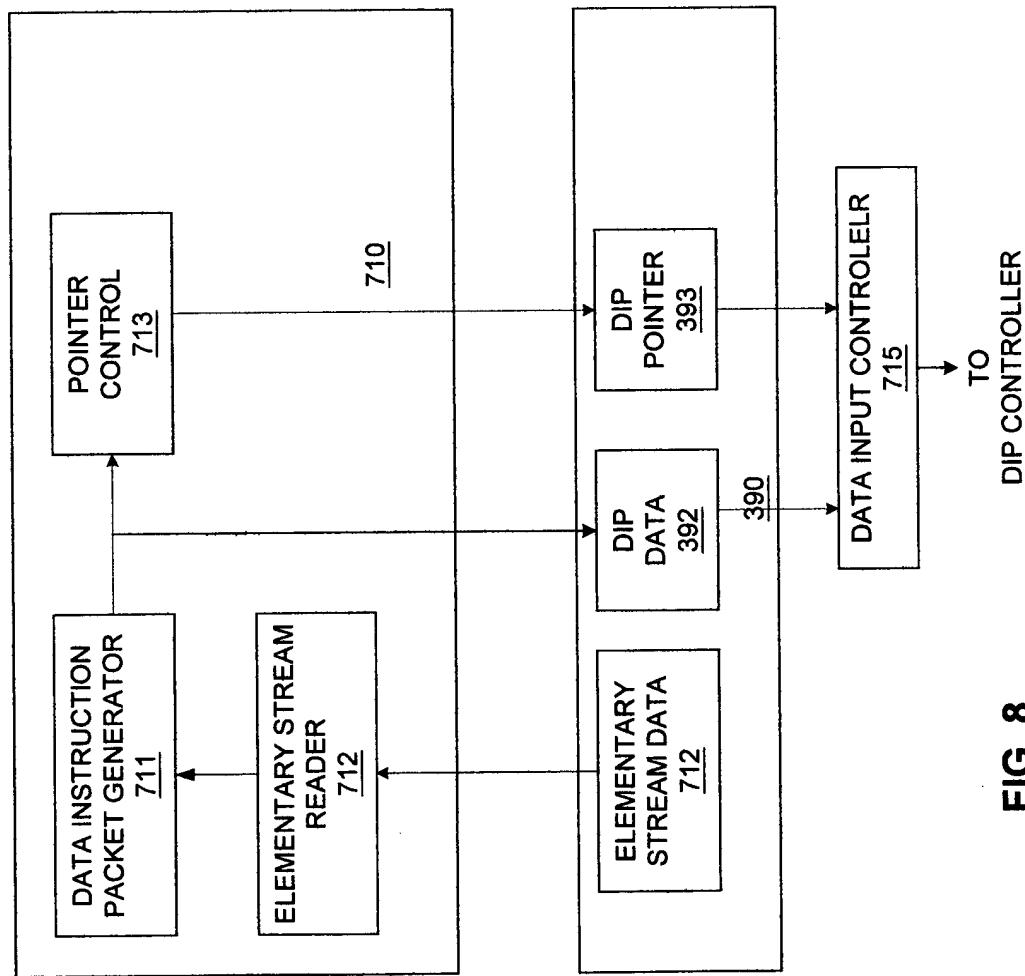


FIG. 8

FIG. 9

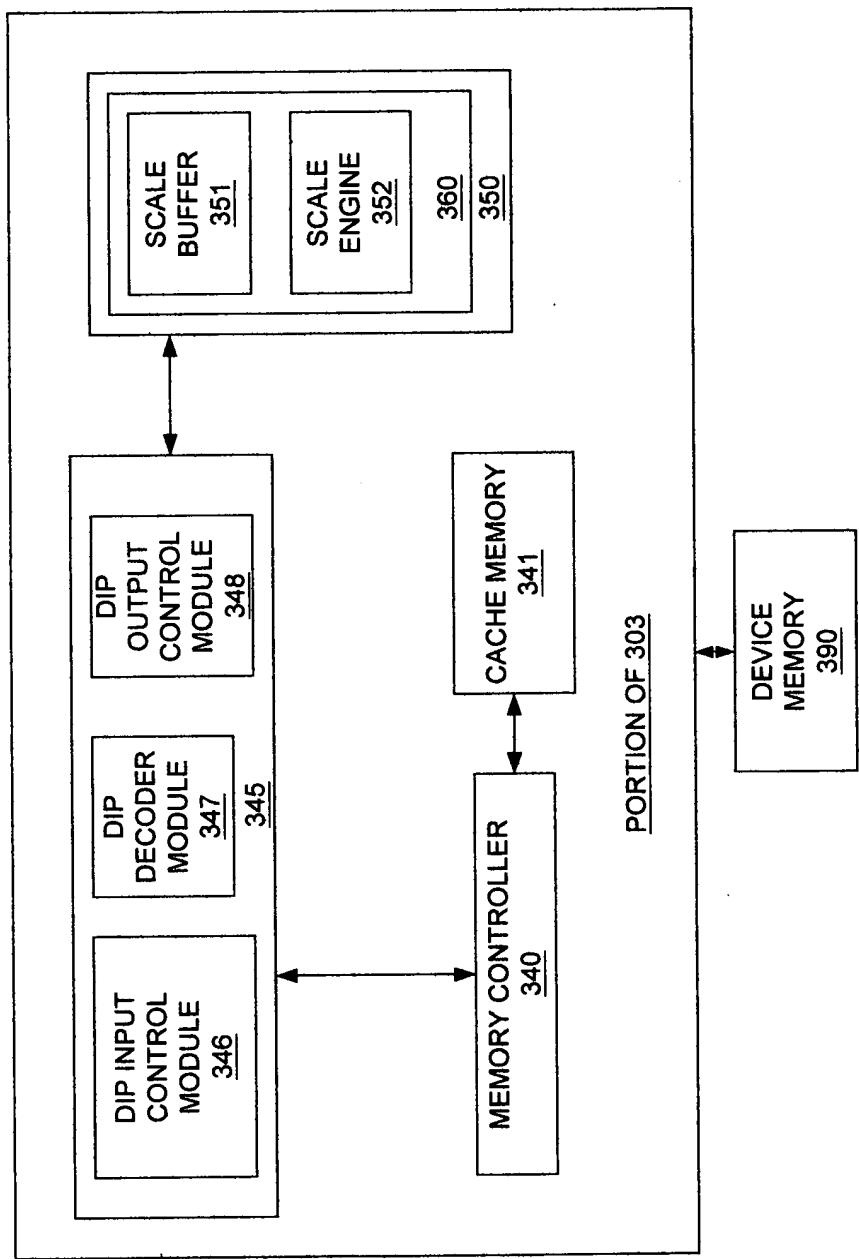


FIG. 9

DMC0		DMC1			
		M(1,0)	M(2,0)	M(3,0)	M(n,0)
M(0,0)				M(n-1,0)	M(n,0)
M(0,1)	M(1,1)	M(2,1)	M(3,1)	M(n-1,0)	M(n,1)
M(0,2)	M(1,2)	M(2,2)	M(3,2)	M(n-1,2)	M(n,2)
M(0,3)	M(1,3)	M(2,3)	M(3,3)	M(n-1,3)	M(n,3)
M(0,4)	M(1,4)	M(2,4)	M(3,4)	M(n-1,4)	M(n,4)
M(0,n)	M(1,n)	M(2,n)	M(3,n)	M(n-1,m)	M(n,m)

FIG. 10

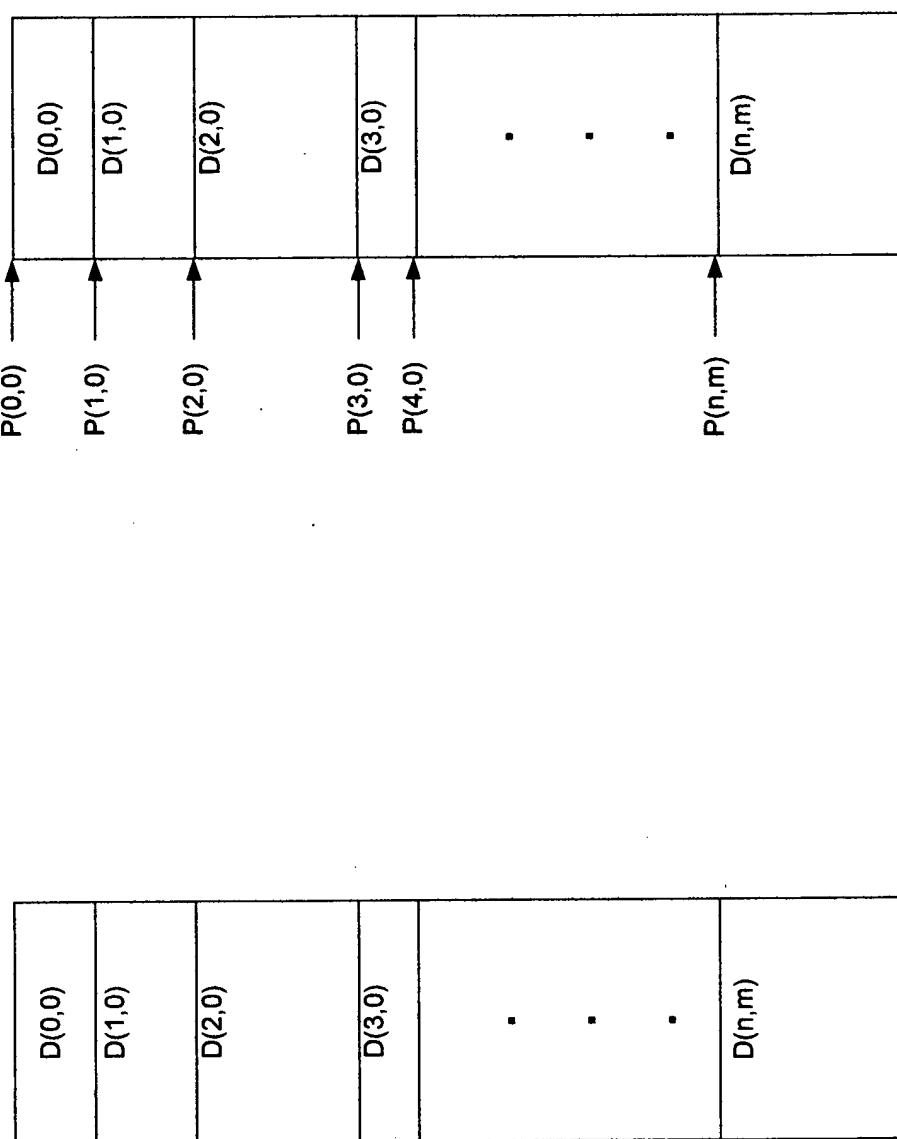


FIG. 11

FIG. 13

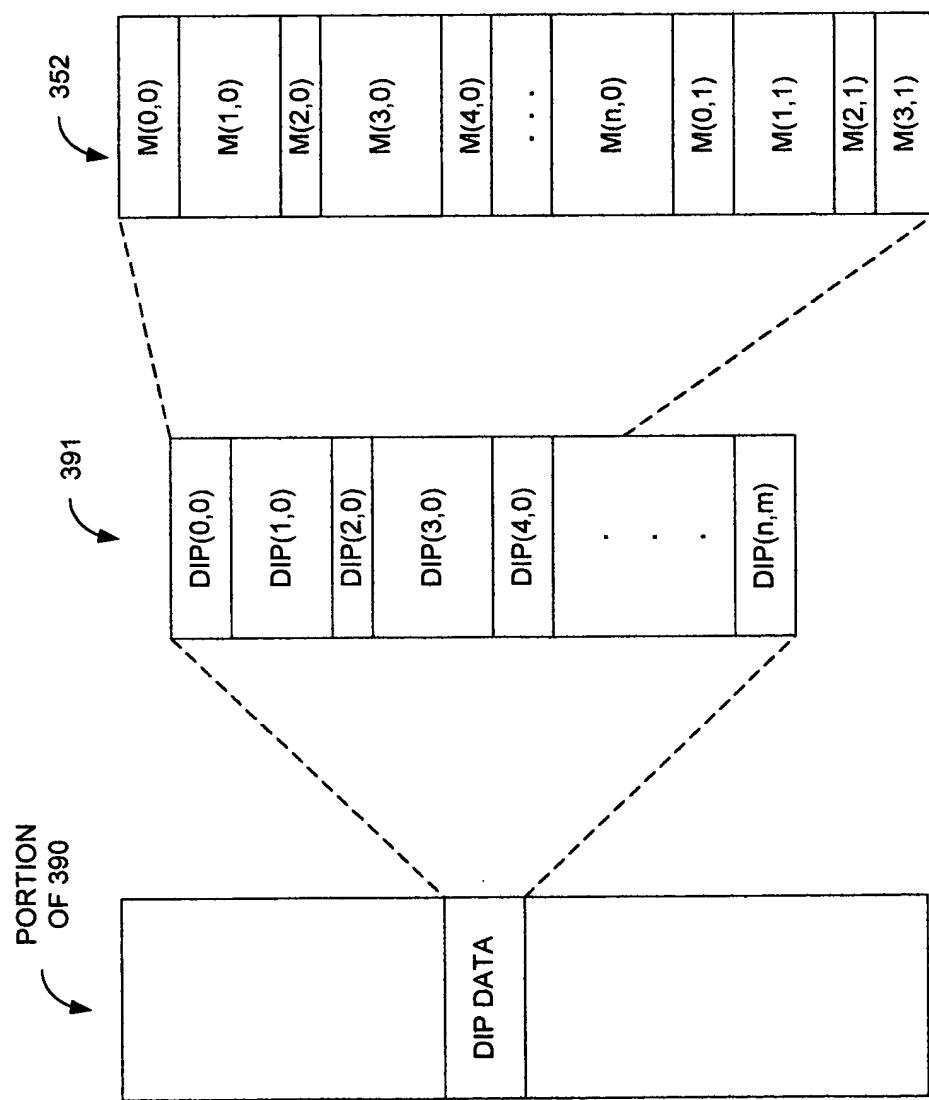
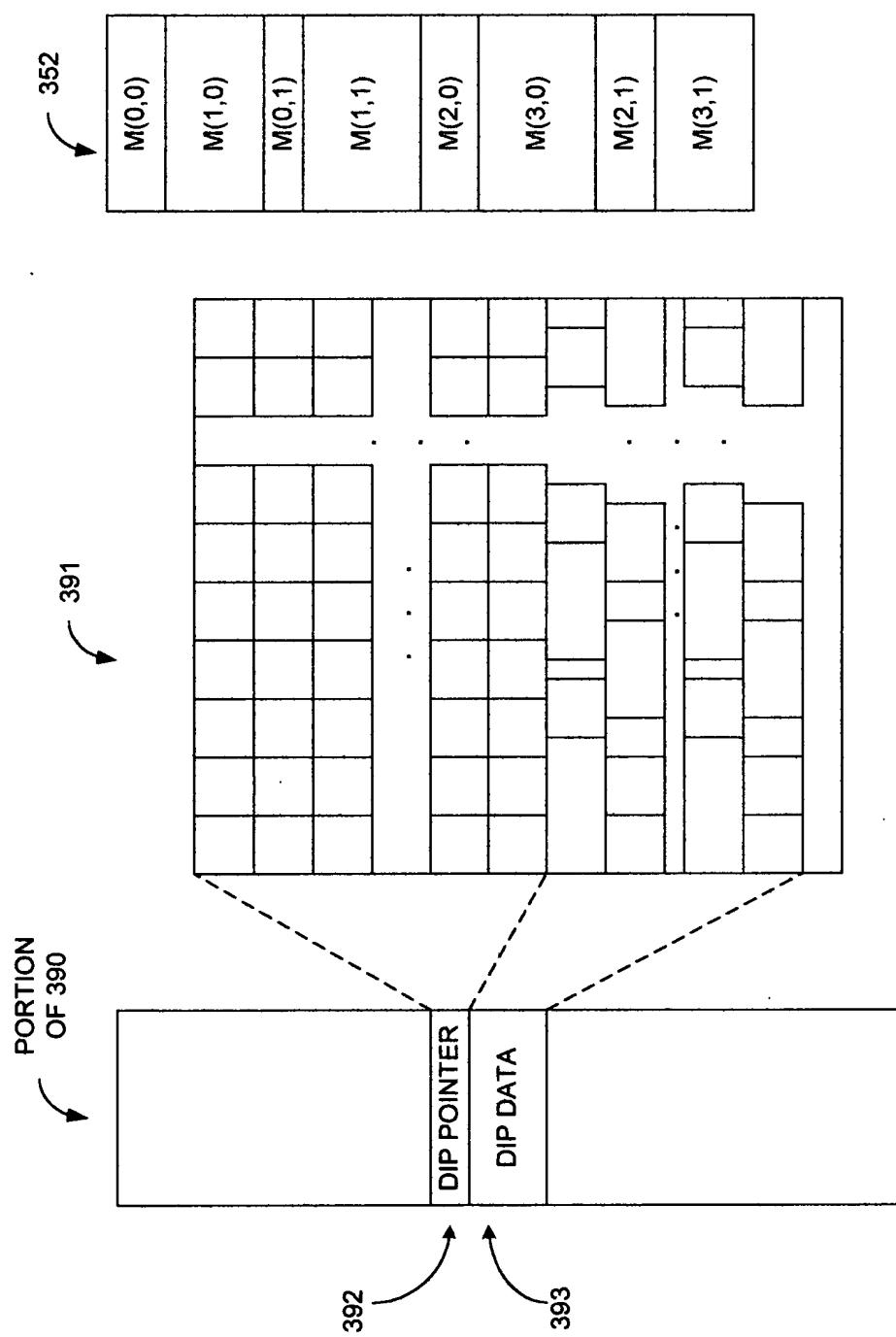
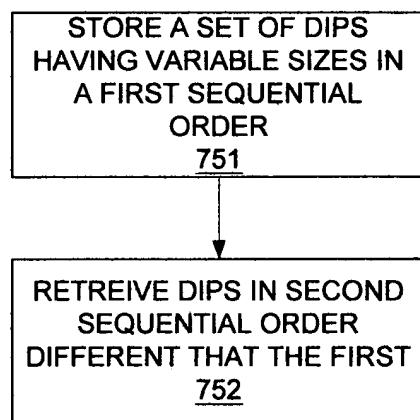
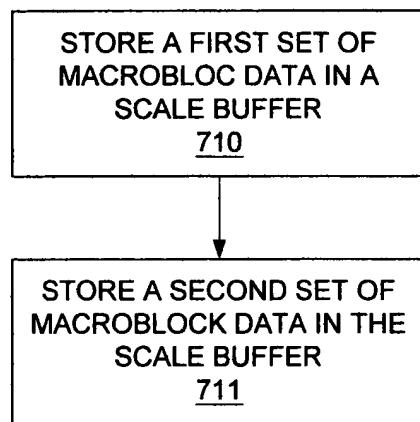


FIG. 12

FIG. 14





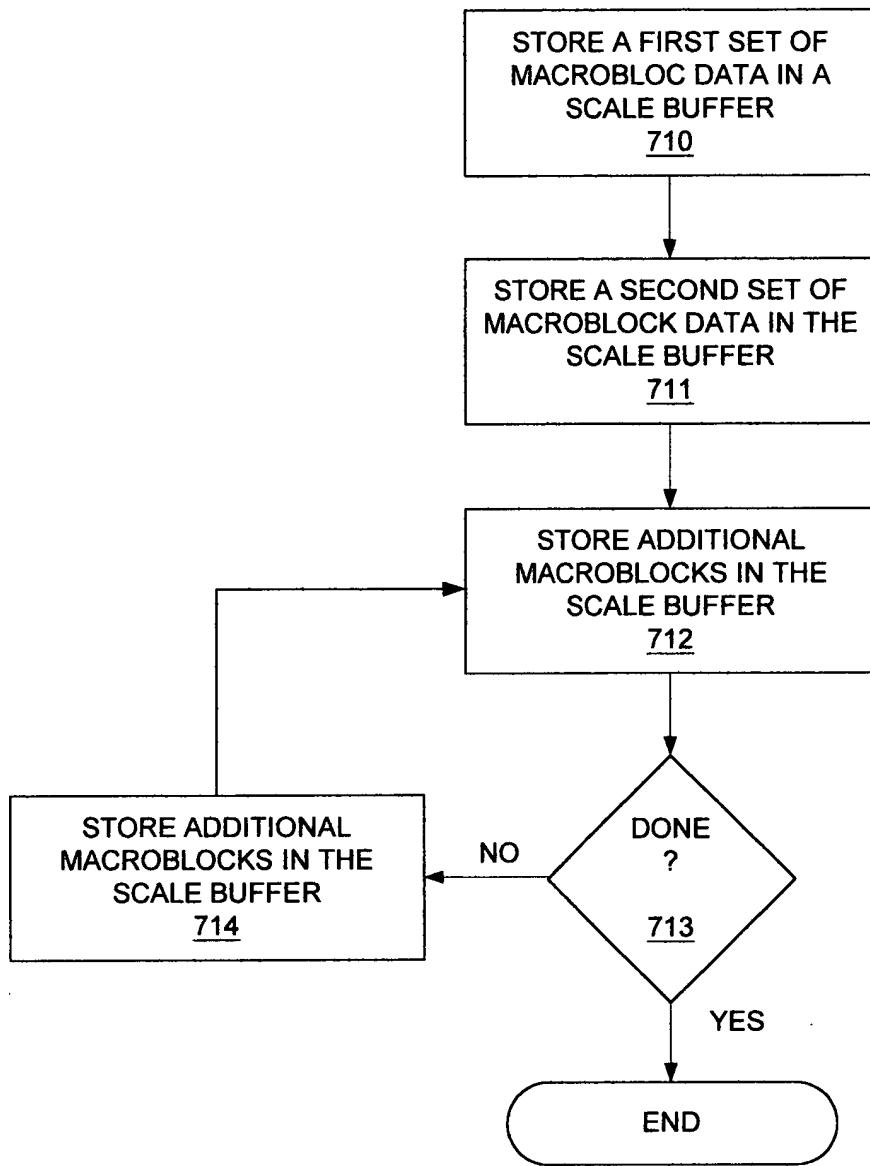


FIG. 16

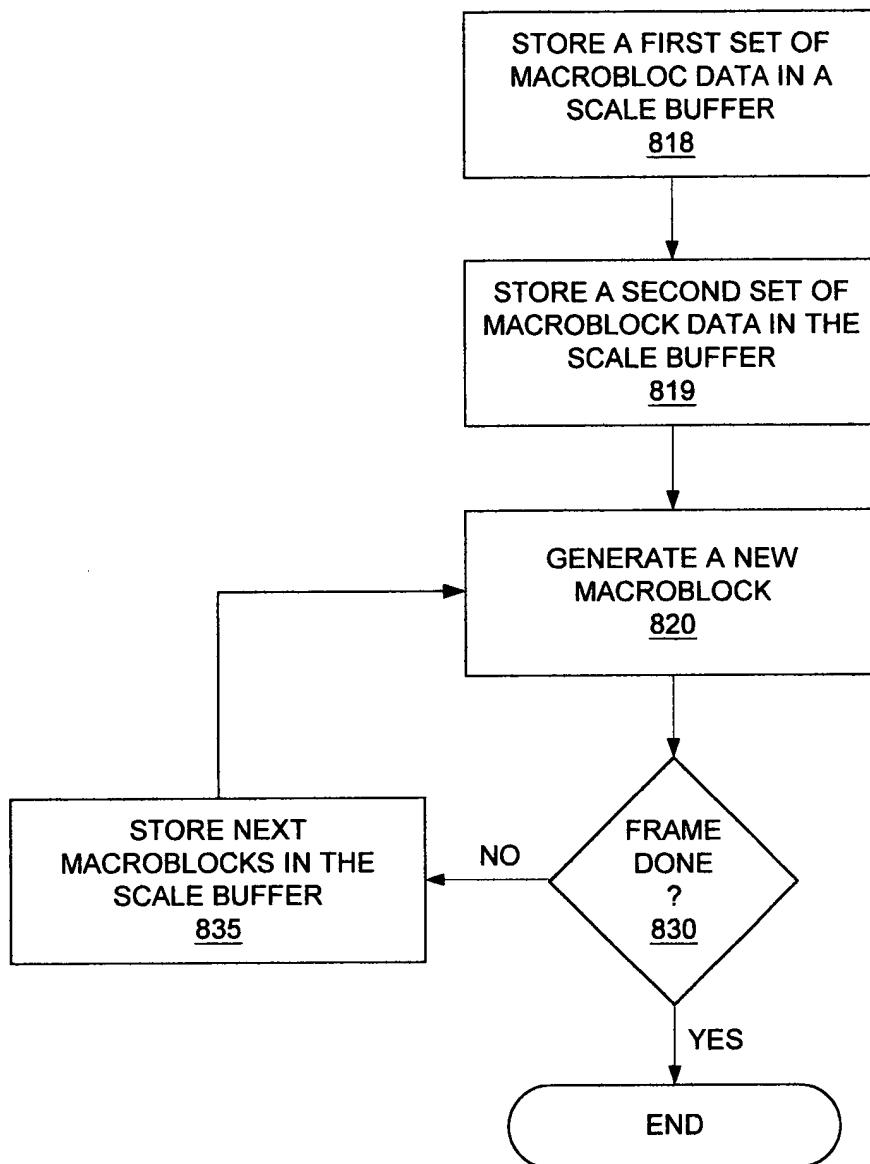


FIG. 17

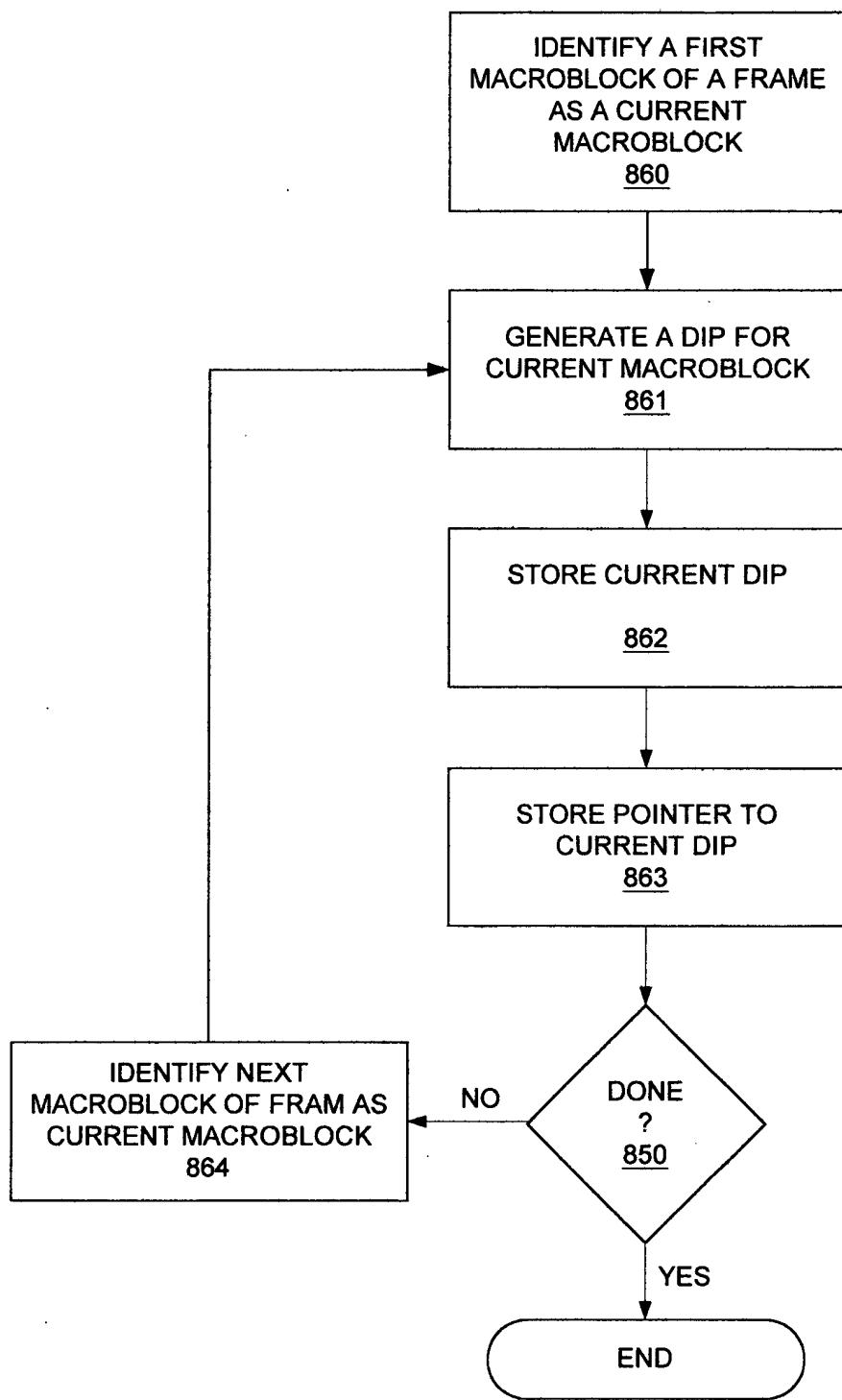


FIG. 18

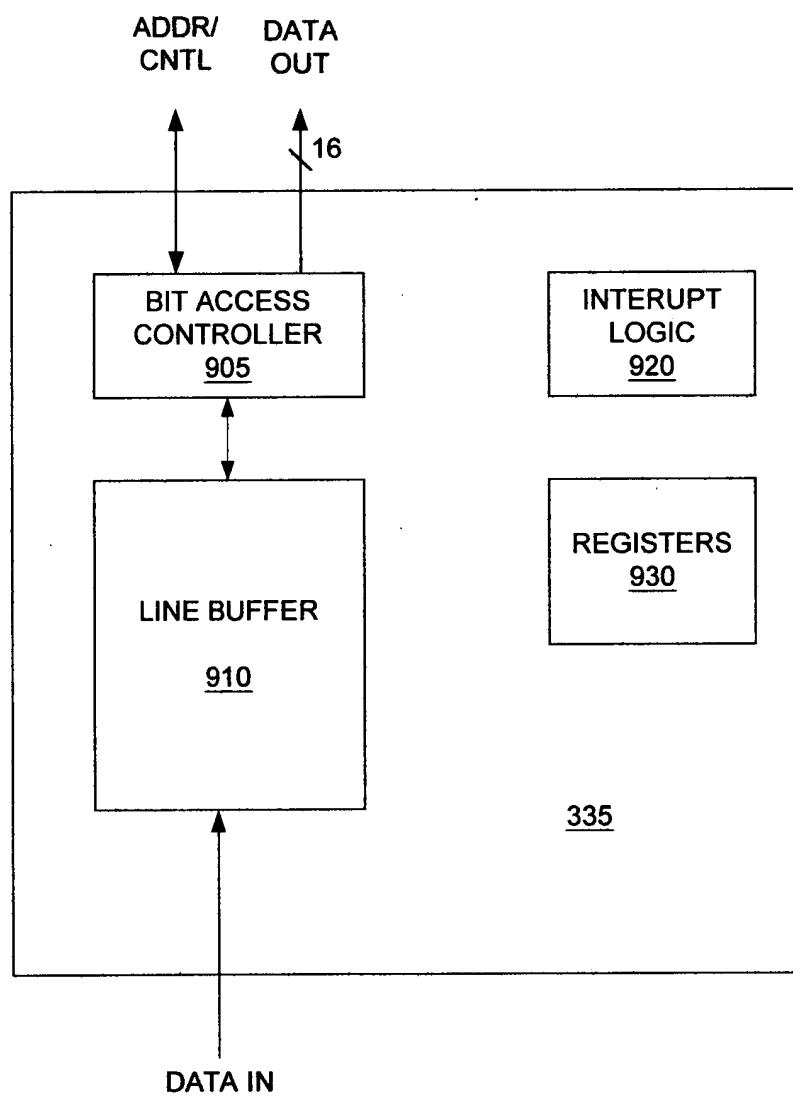


FIG. 20

930 931 932 933 934 935 936 937 938

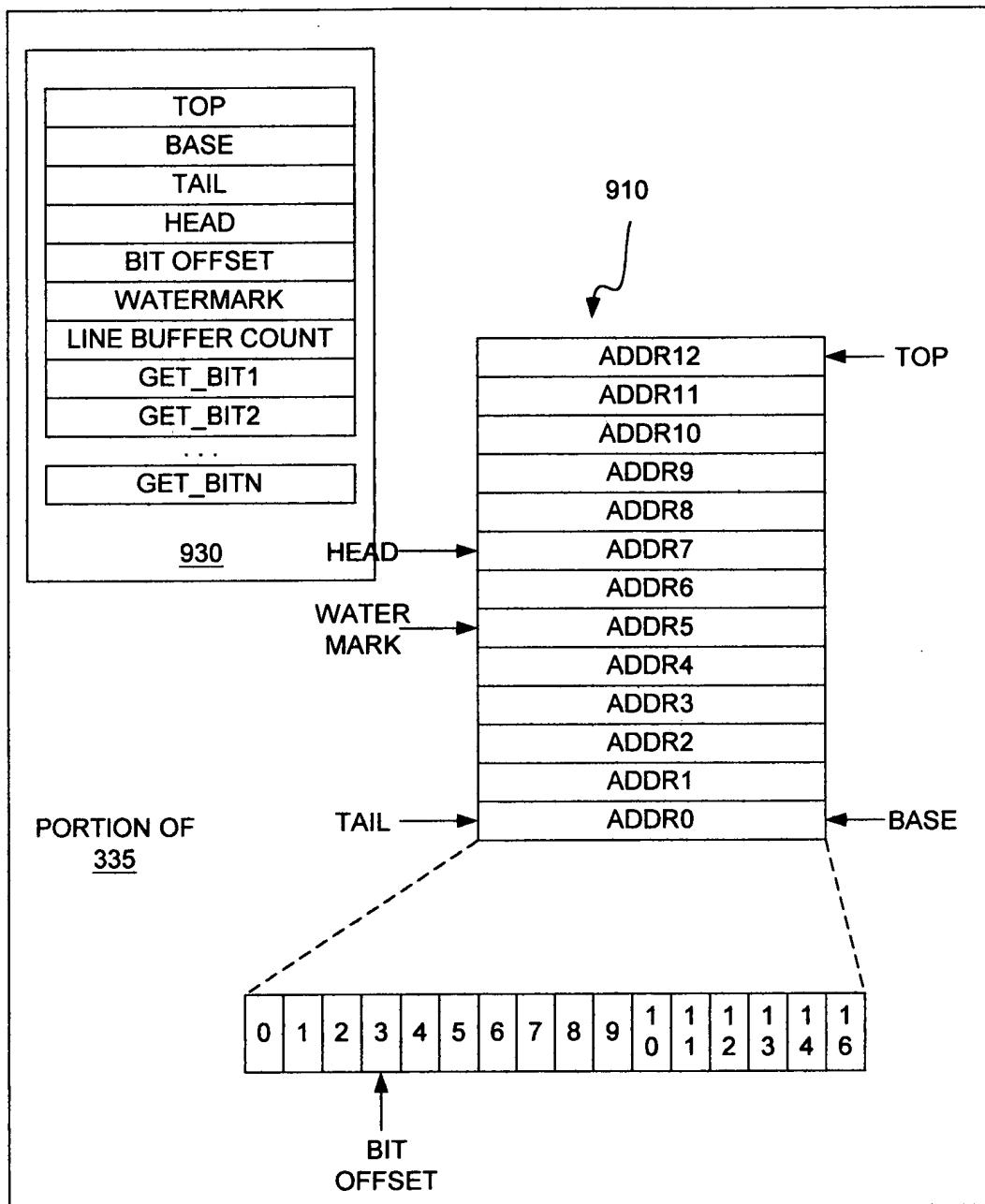


FIG. 21

1000 2000 3000 4000 5000

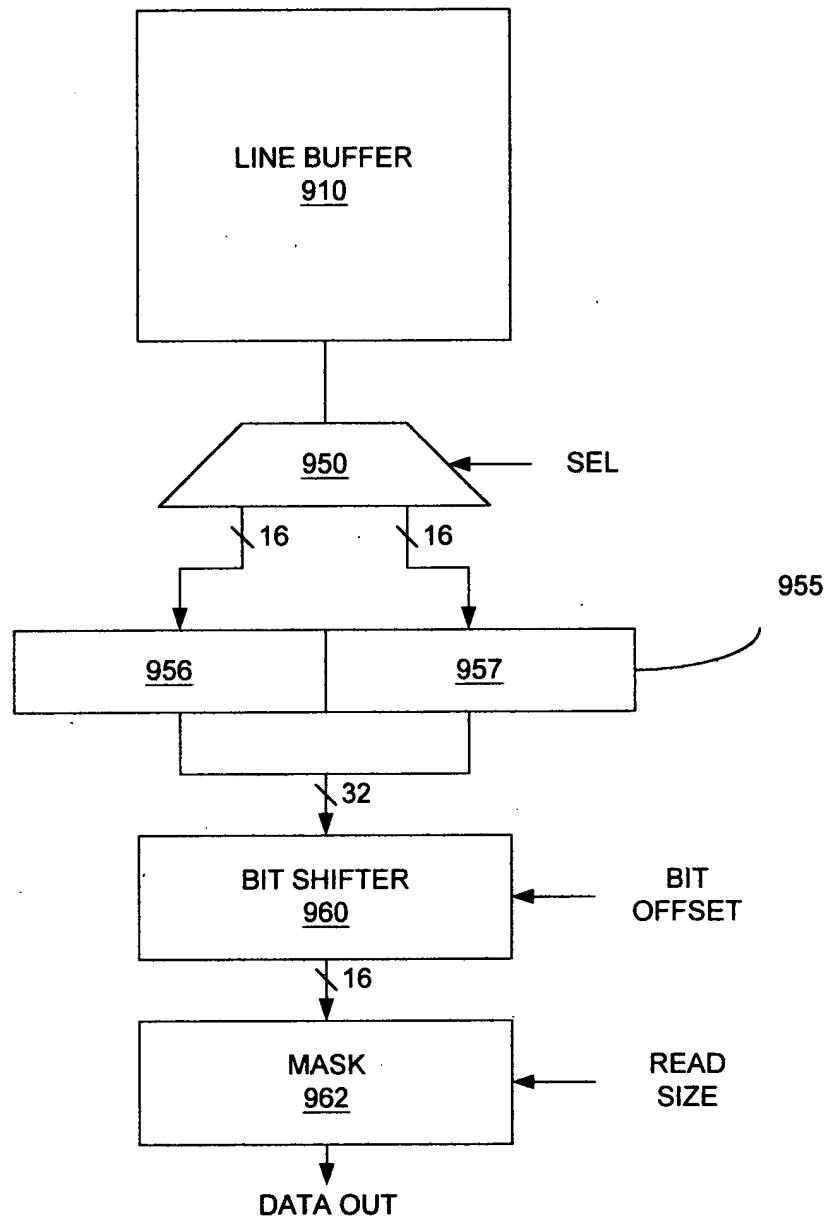


FIG. 22

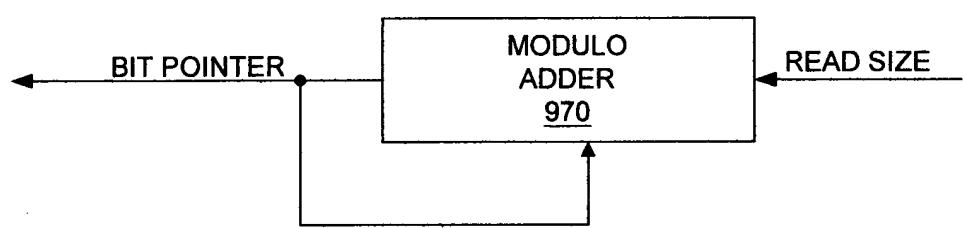


FIG. 23